



PATENT APPLICATION
Docket No: 15436.247.2.1.5

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of)

Lewis B. Aronson et al.)

Serial No.: 10/629,228)

) Art Unit

) 2661

Filed: July 28, 2003)

)

Confirmation No.: 5307)

)

For: TRANSCEIVER MODULE AND INTEGRATED)
CIRCUIT WITH DUAL EYE OPENERS)
AND EQUALIZER)

CERTIFICATE OF DEPOSIT UNDER 37 C.F.R. § 1.8

I hereby certify that the following documents are being deposited with the United States Postal Service as First Class Mail, postage prepaid, in an envelope addressed to: Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450, on the 24th day of February 2004.

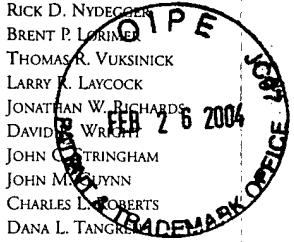
- Transmittal for Information Disclosure Statement (3 pages)
- Information Disclosure Statement (2 pages)
- Form PTO-1449 listing 14 references (2 pages)
- A copy of each of the references list on the Form PTO-1449
- Postcard

Respectfully submitted,

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For: **TRANSCEIVER MODULE AND INTEGRATED CIRCUIT WITH DUAL EYE OPENERS AND EQUALIZER**



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TRANSMITTAL FOR INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
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Sir:

Transmitted herewith for filing and pursuant to 37 C.F.R. § 1.97 is an Information Disclosure Statement, which includes the following statements, if any, required variously by 37 C.F.R. § 1.98:

- Statement of relevance of selected cited references not in the English language which are not translated.
- Statement that selected cited references are substantially cumulative of an enclosed or previously submitted reference.
- Statement that selected cited references were previously cited by or submitted to the United States Patent and Trademark Office in a prior application which is relied upon for an earlier filing date under 35 U.S.C. § 120.

A. Additional Materials Required Due to Content of Information Disclosure Statement

Transmitted are the following documents in addition to the Information Disclosure Statement as required variously under 37 C.F.R. § 1.98:

- Form PTO-1449 listing 14 references submitted for consideration.
- A copy of each of the references listed on the Form PTO-1449.
- English translations of () of the references listed on the Form PTO-1449 which are not in the English language.
- Copies of the following documents from the prosecution of a previous, related application:
 - Form PTO-1449 AND INFORMATION DISCLOSURE STATEMENT; and
 - Form PTO-892

B. Additional Materials Required Due to Timing of Filing of Information Disclosure Statement

The transmitted Information Disclosure Statement is being filed within one (1) of the following four (4) time periods:

- I. Prior to the later of either three (3) months following the filing date or the mailing of a first Office Action. Accordingly, no materials other than those listed above are enclosed.
- II. Following the latter of either three (3) months following the filing date or the mailing of a first Office Action, but before the mailing of a final Office Action or a Notice of Allowance. Accordingly, to secure consideration thereof, one (1) of the following is also enclosed:
 - Promptness Certification; or
 - Check No. _____ in the amount of \$____ constituting the submission fee set forth in 37 C.F.R. § 1.17(p).
- III. After the mailing of a Notice of Allowance, but before payment of the Issue Fee. Accordingly, in order to secure consideration thereof, each of the following are also enclosed:
 - Promptness Certificate;
 - Petition for Consideration; and

- Check No. in the amount of \$ _____ constituting the petition fee set forth in 37 C.F.R. § 1.17(i)(1).
- IV. — After payment of the Issue Fee. Accordingly, in order to secure consideration thereof, each of the following are also enclosed:
 - Petition to Withdraw from Issue; and
 - Check No. _____ in the amount of \$ _____ constituting the petition fee set forth in 37 C.F.R. § 1.17(i)(1).

C. Fees

The Commissioner is hereby authorized to charge payment of or any deficiency in the following fees associated with this communication, or to credit any overpayment thereof, to Deposit Account No. 23-3178. A duplicate copy of this letter is enclosed.

- Any fee required in relation to filing of this letter or any documents transmitted therewith.
- The submission fee set forth in 37 C.F.R. § 1.17(p) in the event that 37 C.F.R. § 1.97(c) applies and the Examiner is not satisfied that any Promptness Certificate submitted meets the requirements of 37 C.F.R. § 1.97(e).
- The submission fee set forth in 37 C.F.R. § 1.17(p).
- The petition fee set forth in 37 C.F.R. § 1.17(i)(1).

Dated this 24th day of February 2004.

Respectfully submitted,



CARL T. REED
Attorney for Applicant
Registration No. 45,454
Customer No. 022913

Applicant: Lewis B. Aronson et al.
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 AND EQUALIZER

Confirmation No.: 5307
 Atty Docket No.: 15436.247.2.1.5
 Group: 2661



INFORMATION DISCLOSURE CITATIONS MADE BY APPLICANT

U.S. Patent Documents

<u>Examiner Initial*</u>	<u>Document Number</u>	<u>Issue Date</u>	<u>Name</u>
1	5,854,704	12/29/1998	Grandpierre
2	6,256,127 B1	07/03/2001	Taylor
3	4,932,038	06/05/1990	Windus
4	2002/0021468 A1	02/21/2002	Kato et al.
5	2002/0060824 A1	05/23/2002	Liou et al.
6	2003/0053170 A1	03/20/2003	Levinson et al.

Foreign Patent Documents

<u>Examiner Initial*</u>	<u>Document Number</u>	<u>Publication Date</u>	<u>Country or Patent Office</u>	<u>Translation</u>

Other Documents
 (including author, title, pertinent pages, etc.)

Examiner
Initial*

7 *LXT16706/16707 SerDes Chipset*, Intel Products,
www.intel.com/design/network/products/optical/phys/lxt16706.htm, April 19, 2002.

8 *LXT35401 XAUI-to-Quad 3.2G Transceiver*, Intel Products,
www.intel.com/design/network/products/optical/phys/lxt35401.htm, April 19, 2002.

Examiner: _____ Date Considered: _____

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____ 9 Texas Instruments User's Guide, *TLK2201 Serdes EVM Kit Setup and Usage*, Mixed Signal DSP Solutions, July 2000.

____ 10 Texas Instruments User's Guide, *TLK1501 Serdes EVM Kit Setup and Usage*, Mixed Signal Products, June 2000.

____ 11 National Semiconductor DS92LV16 Design Guide, *Serializing Made Simple*, February 2002.

____ 12 Vaishali Semiconductor, *Fibre Channel Transceiver*, VN16117, MDSN-0002-02, 08/09/2001.

____ 13 Fairchild Semiconductor, Application Note 77, *CMOS, the Ideal Logic Family*, January 1983.

____ 14 Analog Target Specification, Annex 48B, Published by IEEE New York, 05/2001, pp. 6-14.

References Cited by Applicants

While the filing of Information Disclosure Statements is voluntary, the procedure is governed by the guidelines of Section 609 of the Manual of Patent Examining Procedure and 37 C.F.R. §§ 1.97 and 1.98. To be considered a proper Information Disclosure Statement, Form PTO-1449 shall be accompanied by a copy of each listed patent or publication or other item of information and a translation of the pertinent portions of foreign documents (if an existing translation is readily available to the applicant), an explanation of relevance of each reference not in the English language, and should be submitted in a timely manner as set out in MPEP Sec. 609.

Examiners will consider all citations submitted in conformance with 37 C.F.R. § 1.98 and MPEP Sec. 609 and place their initials adjacent the citations in the spaces provided on this form. Examiners will also initial citations not in conformance with the guidelines which may have been considered. A reference may be considered by the Examiner for any reason whether or not the citation is in full conformance with the guidelines. A line will be drawn through a citation if it is not in conformance with the guidelines AND has not been considered. A copy of the submitted form, as reviewed by the Examiner, will be returned to the applicant with the next communication. The original of the form will be entered into the application file.

Each citation initialed by the Examiner will be printed on the issued patent in the same manner as references cited by the Examiner on Form PTO-892.

The reference designations "A1," "A2," etc. (referring to Applicant's reference 1, Applicant's reference 2, etc.) will be used by the Examiner in the same manner as Examiner's reference designations "A," "B," "C," etc. on Office Action Form PTO-1142.